Insulated Gate Bipolar Transistor Type SA45RO0800GG

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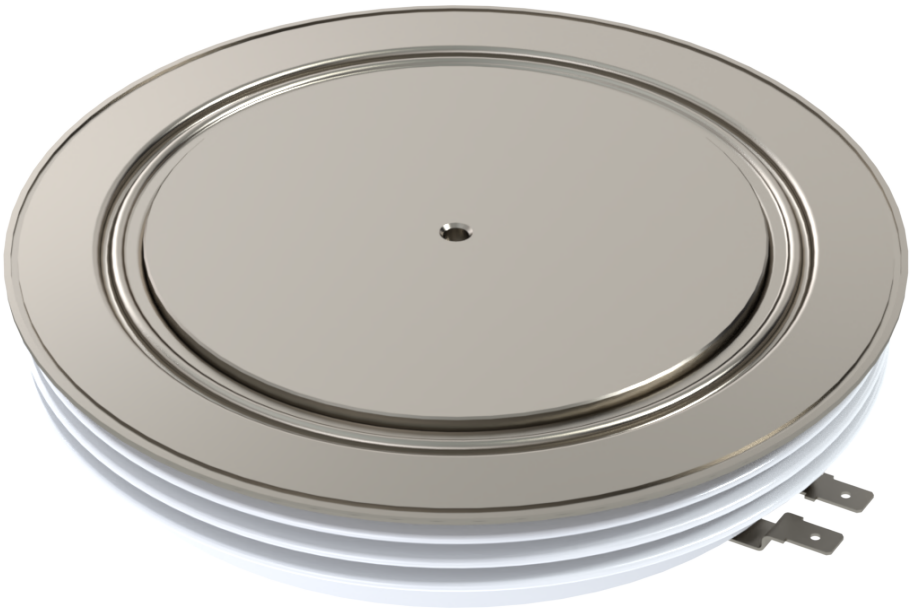

ORDERING INFORMATION						(Please quote 12 to 15 digit code as below)
SA	45	RO	0800	G	G	
-	Voltage Code	Outline Code	Current code	Type code	Special code	Optional code

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Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector-emitter voltage	4500	V
V_{DClink}	Permanent DC voltage for 100 FIT failure rate	2800	V
V_{GES}	Peak gate - emitter voltage	±20	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
I_C	DC collector current, IGBT	800	A
I_{CRM}	Repetitive peak collector current, t_p = 1ms, IGBT	1600	A
$I_{F(DC)}$	Continuous DC forward current, Diode	800	A
I_{FRM}	Repetitive peak forward current, t_p = 1ms, Diode	1600	A
I_{FSM}	Peak non-repetitive surge t_p = 10ms, V_{RM} = 60%V_{RRM}, Diode (note 4)	5720	A
I_{FSM2}	Peak non-repetitive surge t_p = 10ms, $V_{RM} \leq$ 10V, Diode (note 4)	6300	A
P_{MAX}	Maximum power dissipation, IGBT (note 2)	6.4	kW
P_D	Maximum power dissipation, Diode (note 2)	4.05	kW
$(di/dt)_{cr}$	Critical diode di/dt (note 3)	2000	A/µs
T_j	Operating temperature range	-40 to +125	°C
T_{stg}	Storage temperature range	-40 to +125	°C
note 1)	Unless otherwise indicated T_j = 125°C		
note 2)	T_{sink} = 25°C, doublesidecooled		
note 3)	Maximum commutation loop inductance 400 nH		
note 4)	Half-sinewave, 125°C T_j initial.		

IGBT Characteristics

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
$V_{CE(sat)}$	Collector-emitter saturation voltage	I_C = 800A, V_{GE} = 15V, T_j = 25°C	-	2.75	3.2	V
		I_C = 800A, V_{GE} = 15V	-	3.50	3.9	V
V_{T0}	Threshold voltage	Current range: 267A-8000A	-	-	1.74	V
r_T	Slope resistance		-	-	2.70	mΩ
$V_{GE(TH)}$	Gate threshold voltage	V_{CE} = V_{GE}, I_C = 85mA	-	5.2	-	V
I_{CES}	Collector-emitter cut-off current	V_{CE} = V_{CES}, V_{GE} = 0V	-	20	50	mA
I_{GES}	Gate leakage current	V_{GE} = ±20V	-	-	±15	µA
C_{ies}	Input capacitance	V_{CE} = 25V, V_{GE} = 0V, f=1MHz	-	135	-	nF
$t_{d(on)}$	Turn-on delay time	I_C = 800A, V_{CE} = 2800V, di/dt = 1700A/µs, V_{GE} = ±15V, L_S = 200nH, $R_{g(ON)}$ = 6.8Ω, $R_{g(OFF)}$ = 10Ω, C_{GE} = 90nF Integral diode used aus freewheel diode (notes 1 & 2)	-	1.8	-	µs
t_r(V)	Rise time		-	3.3	-	µs
$Q_{g(on)}$	Turn-on gate charge		-	5	-	µC
E_{on}	Turn-on energy		-	6.3	-	J
$t_{d(off)}$	Turn-off delay time		-	3.0	-	µs
t_f(I)	Fall time		-	2.4	-	µs
$Q_{g(off)}$	Turn-off gate charge		-	8	-	µC
E_{off}	Turn-off energy		-	3.7	-	J
I_{SC}	Short circuit current	V_{GE} = +15V, V_{CC} = 2800V, $V_{CEmax} \leq V_{CES}$, $t_p \leq 10µs$	-	2800	-	A
note 1)	C_{GE} is additional gate–emitter capacitance added to output of gate drive					
note 2)	Figures 6 to 9 are obtained using integral Diode aus freewheeling diode					

Diode Characteristics

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
V_{FM}	Forward voltage	I_F = 800A, T_j = 25°C	-	3.35	3.6	V
		I_F = 800A	-	3.50	3.8	V
V_{T0}	Threshold Voltage	Current range 267A-800A	-	-	2.05	V
r_T	Slope resistance		-	-	2.19	mΩ
I_{RRM}	Peak reverse recovery current	I_F = 800A, V_R = 2800V,V_{GE} = –15V, di/dt = 1700A/µs	-	800	-	A
Q_{rr}	Recovered charge		-	1020	-	µC
t_{rr}	Reverse recovery time, 50% Chord		-	1.5	-	µs
E_r	Reverse recovery energy		-	1.2	-	J

Thermal Characteristics

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
R_{thJK}	Thermal resistance, junction to sink, IGBT	Double side cooled	-	-	15.6	K/kW
		Collector side cooled	-	-	25.4	K/kW
		Emitter side cooled	-	-	40.5	K/kW
R_{thJK}	Thermal resistance, junction to sink, Diode	Double side cooled	-	-	24.7	K/kW
		Cathode side cooled	-	-	37.9	K/kW
		Anode side cooled	-	-	70.8	K/kW
F	Mounting force	Note 2	25	-	35	kN
W_t	Weight		-	1200	-	g
note 1)	Unless otherwise indicated T_j = 125°C					
note 2)	Consult application note for detailed mounting requirements					

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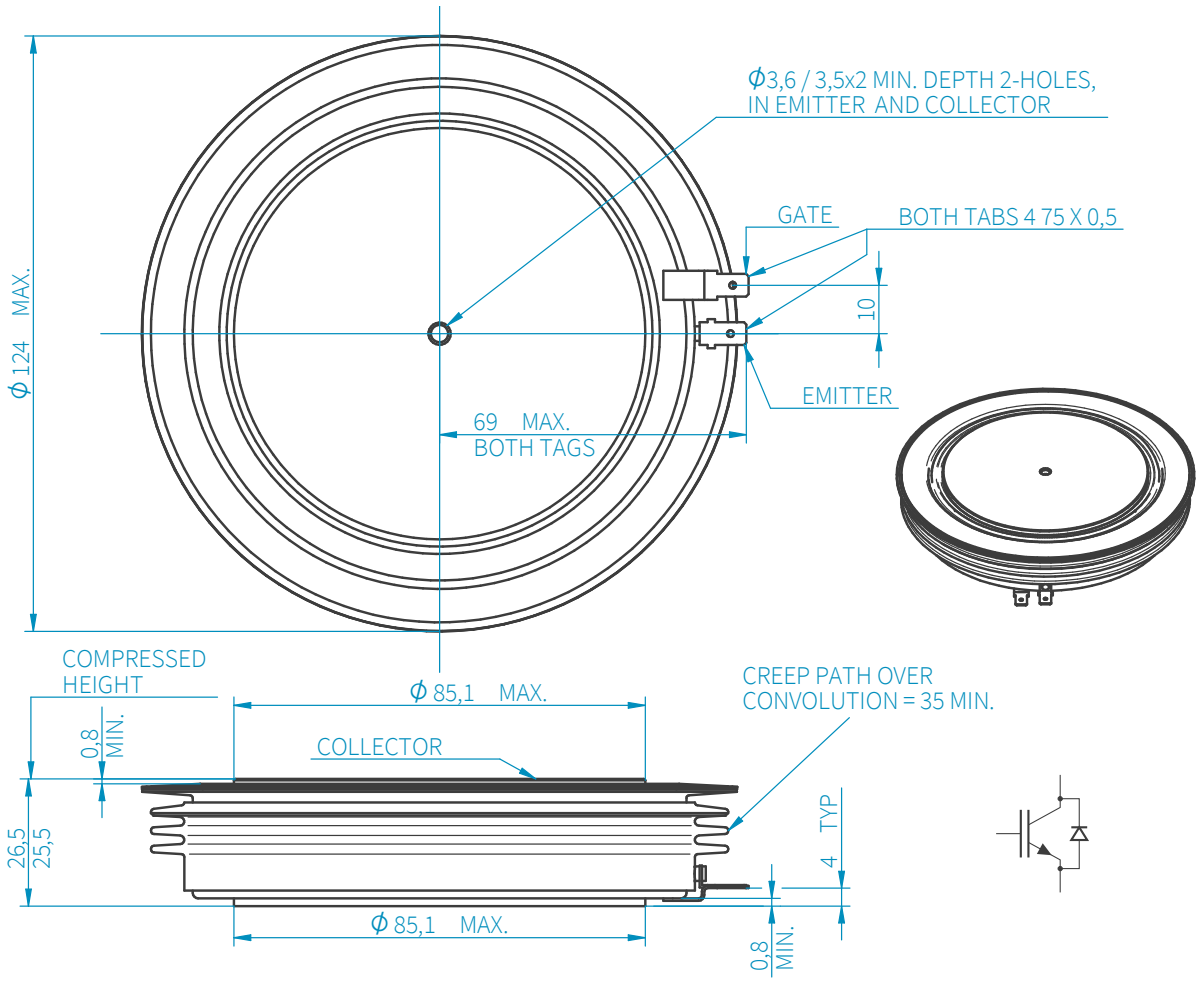
Outline Drawing



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